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6-30-98jc525 U.S. PTO
09/028276

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application Of:

Shigeru ATSUMI

Group Art Unit: TBA

Serial No.: To Be Assigned

Atty. Docket No. 1701.73982

Filed: Concurrently Herewith

Examiner: TBA

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND FLASH EEPROM

INFORMATION DISCLOSURE STATEMENTAssistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

Pursuant to 37 C.F.R. §1.56 and in compliance with 37 C.F.R. §1.97, Applicant herewith submits, in lieu of a PTO Form 1449, a Background Art Information paper. As you will note, the Applicant is not aware of any prior art at this time.

This statement should not be construed as a representation that more material information does not exist or that an exhaustive search of the relevant art has been made.

Respectfully submitted,

BANNER & WITCOFF, LTD.

By:

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for Joseph M. Potenza
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